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		APPLICANT DeWitt et al.			
		FILING DATE	GROUP ART UNIT		
U.S. PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
<i>JAL</i>	5,621,886	04/15/1997	Alpert et al.	395/183.14	
<i>JAL</i>	5,659,679	08/19/1997	Alpert et al.	395/183.01	
<i>JAL</i>	5,715,440	02/03/1998	Ohmura et al.	395/580	
<i>JAL</i>	5,740,413	04/14/1998	Alpert et al.	395/568	
<i>JAL</i>	5,938,778	08/17/1999	John, Jr. et al.	714/45	
<i>JAL</i>	6,094,729	07/25/2000	Mann	714/25	
FOREIGN PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO
OTHER PRIOR ART (including author, title, date, pertinent page, etc.)					
<i>JAL</i>	AIX Version 4.3 Kernel and Subsystems Technical Reference, Vol. 1, "pinu Kernel Service", IBM Corp., 1997.				
<i>JAL</i>	<i>Itanium™ Processor Microarchitecture Reference for Software Optimization</i> , Intel Corp., 03/2000, pp. 1-28.				
<i>JAL</i>	<i>Itanium™ IA-64 Architecture Software Developer's Manual, Vol. 4: Itanium™ Processor Programmer's Guide</i> , Revision 1.1, Intel Corp., 07/2000, pp. 6-1 to 6-32.				
<i>JAL</i>	<i>Itanium™ IA-64 Architecture Software Developer's Manual, Vol. 1: IA-64 Application Architecture</i> , Revision 1.1, Intel Corp., 07/2000, pp. 2-1 to 2-8, 3-1 to 3-20, 4-1 to 4-35, 9-1 to 9-16, 10-1 to 10-16.				
<i>JAL</i>	<i>Itanium™ IA-64 Architecture Software Developer's Manual, Vol. 2: IA-64 System Architecture</i> , Revision 1.1, Intel Corp., 07/2000, pp. 2-1 to 2-4, 3-1 to 3-22, 5-1 to 5-36, 7-1 to 7-14.				
DATE CONSIDERED <i>10/27/2004</i>	EXAMINER <i>Joshua John</i>				
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